TRUST YOUR SOFTWARE

seL4 PLATFORM INTEGRATION

If you need true security, half-measures just won't do.

Complete formal verification is the only known way to guarantee that a system is free of programming errors, and one of the achievements of seL4.

DornerWorks is a leader in accelerating integration of seL4 as the trusted software base for your product.

FRUSTRATION-FREE EMBEDDED SECURITY
SUPERCHARGED WITH THE SEL4 MICROKERNEL

The complexity of embedded security shouldn't hold your team back from innovation.

seL4 has already been proven secure, so you don't have to do the extreme math yourself. DornerWorks will help you fill in the gaps to enhance your security story.

INCLUDED IN THE
QUICK START PACKAGE

seL4 on a working platform
- Build environment and instructions.
- ARM target selection,
- seL4 & CAmkES examples.

Training workshop
A one-day, hands-on tutorial-style workshop with your entire team, covering a range of seL4 and virtualization topics, and more,

Consulting report
Clear, written recommendations from our experts to further guide you through implementing seL4 with your product.

KICKSTART
DEVELOPMENT

Getting your feet wet with complex technology can be intimidating. DornerWorks has been working with seL4 for several years, supported by Small Business Innovative Research contract awards from DARPA.

We understand how to leverage the seL4 microkernel to support your needs, including customization, certification, and optimization.

With our seL4 Quick Start Package, you can rely on our expertise and win back freedom to focus on your product.

VMM MODE
ON ARM v8

- A DoD customer was interested in using seL4 as a hypervisor to provide robust security and isolation and they wanted to run on the latest ARMv8 platform, due to its performance and feature set.

- DornerWorks ported the existing ARMv7 virtualization support to an ARMv8 implementation, and provided BSP layers for the Xilinx Zynq UltraScale+ MPSoC and the NXP i.MX8 platforms.

- The prototype showed the isolation benefits that the customer required on the platform they needed.

seL4 PORT TO
ZUS+ MPSoC

The Zynq UltraScale+ is a Multi-Processor System on a Chip that has a quad-core Cortex-A53, a dual-core Cortex-R5, a GPU, and an FPGA. This chip is Xilinx's most secure solution yet, with features like Secure Boot, Xilinx Memory Protection Unit (XMPU), and Xilinx Peripheral Protection Unit (XPPU).

DornerWorks ported seL4 to the MPSoC, allowing it to be run as a Hypervisor, which is another means of reinforcing the budding seL4 ecosystem, by allowing guest operating systems to run on top of seL4.

GET STARTED TODAY

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Contact us for a free consultation and together we will determine a solution that fits your product security goals.

SCHEDULE A CONSULTATION

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