



XILINX & DORNERWORKS ENABLE VERSATILE, REAL-TIME VIDEO DEMONSTRATION

This demonstrates DornerWorks design and implementation of software controllable FPGA-based networked video systems on a Xilinx Zynq UltraScale+ MPSoC ZCU106.

The ZCU106 accelerates designs for video conferencing, surveillance, Advanced Driver Assisted Systems (ADAS), and streaming and encoding applications.

Xilinx Zynq UltraScale+ MPSoC DornerWorks Engineering **SW-DEFINED FPGA VIDEO**



DornerWorks is developing a video demo to use with Xilinx FAE/TSE teams and mutual customers.

DornerWorks will be showing this demo at the upcoming Xilinx international sales conference, Xilinx Developers Forum, and Xilinx Security Working Group events, and we look forward to meeting with you.

We welcome your feedback regarding customers that would have interest in the demo capabilities like **Video Codec Unit (VCU)**, **PCIe**, **Time-Sensitive Networking (TSN)**, or **Machine Learning (ML)**.



Multi-stream capable DisplayPort input

Provides up to 4 input video streams over a single DisplayPort interface



Software defined video overlay

Textual and menu framework developed for fully dynamic on-screen displays



Alpha-blended mixing

Combine DisplayPort input video streams and software defined overlay



Mixed video output

HDMI, DisplayPort, HD-SDI, or custom analog FMC



Mixed video encoding

Using the VCU and streamed over Ethernet using DornerWorks AVB IP

SCHEDULE A CONSULTATION

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