MOVE AHEAD FASTER FPGA DESIGN & ARCHITECTURE

Designing a new FPGA can rob your team of development hours and resources.

DornerWorks guides companies to success with engineered solutions for sea, land, air, and space, using FPGAs, hardware-configurable SOCs and custom IP.

Those companies aren't using up time and energy trying to implement new technology.

They are leading the way.

ACCELERATE YOUR FPGA DEVELOPMENT



DornerWorks can architect data pipelines to your requirements, help you integrate 3rd party or custom IP, and optimize your software and FPGA systems to work seamlessly.

Technology complexities shouldn't derail your innovation.

Partner with DornerWorks and you will reach market quickly, launch products that stand out, and understand how they work.

Schedule a consultation with us today so you can get back to growing your business.



DEVELOP QUICKLY RETAIN KNOWLEDGE

DornerWorks provides the knowledge base and training to help you configure your FPGAs on your own.

DIVERSE & VERSATILE PLATFORMS • Real-time video

- - Time-synchronous networking
 Radio/RFSoC
 - Machine learning
 - DSP

MULTI-DISCIPLINE DESIGN PARTNER

- Turnkey solutions
 - Design migration
- Algorithm implementation & acceleration
- Hypervisor-based separation
- Video processing IP
- Custom IP

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DornerWorks is a professional, flexible, AND AN ESSENTIAL PART OF OUR TEAM 11

DEFENSE-CLASS NETWORKING

The DornerWorks MAF Endpoint FPGA IP was implemented on a system that detects and mitigates the threat of oncoming airborne explosive devices around the perimeter of military ground vehicles.

- Meets demands for MAPS.
- gPTP module with +/-8ns accuracy.
- Credit-based, strict-priority scheduling.

The IP was developed for Xilinx FPGAs. As part of the MAC IP, it enables support for multiple types of traffic over a <u>common network to reduce costs</u>.

VIDEO PROCESSING

An autonomous automotive company relied on DornerWorks' FPGA design and IP implementation, for an advanced driver-assistance system (ADAS) that optimized data ingestion from up to 12 simultaneously streaming cameras.

- Cameras interface with MIPI CSI-2 and parallel I/O.
- > 30 gbps of RAW video at 30 fps.
 Camera resolutions up to 12
- mega-pixel.
- > 55 Gbps of RGB and grayscale video over multiple PCIe 3.0 interfaced to the rest of the system for additional processing and logging.



A major defense contractor, with FPGA engineering guidance from DornerWorks, developed a system that searches for and identifies moving targets with concealed explosive devices.

- Successfully reduced the size, weight, and power costs of an existing radar system on an AVNET MicroZed with a Zynq-7000 device.
- Optimized data throughput speeds.
- Integrated the customers' existing ADC capture logic to receive ADC samples.

GET STARTED TODAY

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Connect with us now. Together we will map out a plan that meets your product goals and helps you lead the market.





SCHEDULE A CONSULTATION

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